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L13	717/159,163,136.ccls.	441	L13
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DB=TDBD; PLUR=YES; OP=ADJ			
L11	(creat$ or generat$ or develop$ or mak$) near4 (relocation$ near4 instruction$) (compil$ Or assembl$ or machine$ or object$) near8 (code$ or instruction$ or program$)	0	L11
DB=DWPI; PLUR=YES; OP=ADJ			
L10	(creat$ or generat$ or develop$ or mak$) near4 (relocation$ near4 instruction$) (compil$ Or assembl$ or machine$ or object$) near8 (code$ or instruction$ or program$)	0	L10
DB=JPAB; PLUR=YES; OP=ADJ			
	(creat$ or generat$ or develop$ or mak$) near4 (relocation$ near4		

| L9 | instruction$) (compil$ Or assembl$ or machine$ or object$) near8 (code$ or instruction$ or program$) | 0 | L9 |

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| L8 | (creat$ or generat$ or develop$ or mak$) near4 (relocation$ near4 instruction$) (compil$ Or assembl$ or machine$ or object$) near8 (code$ or instruction$ or program$) | 0 | L8 |

DB=PGPB; PLUR=YES; OP=ADJ

| L7 | (creat$ or generat$ or develop$ or mak$) near4 (relocation$ near4 instruction$) (compil$ Or assembl$ or machine$ or object$) near8 (code$ or instruction$ or program$) | 1 | L7 |

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L6	(replac$ near9 (relocation$ near5 instruction$))	2	L6
L5	(replac$ near9 relocation$)	189	L5
L4	L3 and (replac$ near9 relocation$)	0	L4
L3	L2 and (compil$ Or assembl$ or machine$ or object$) near8 (code$ or instruction$ or program$)	13	L3
L2	(creat$ or generat$ or develop$ or mak$) near4 (relocation$ near4 instruction$)	13	L2
L1	(creat$ or generat$) near4 (relocation$ near4 instruction$) and (object or machine$) near4 (code$ or program$)	6	L1

END OF SEARCH HISTORY

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Terms used
generate and **relocation instruction** and **mathc** and **sequence** and **indicator**

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21 Design and implementation of a diagnostic compiler for PL/I
Richard W. Conway, Thomas R. Wilcox
March 1973 **Communications of the ACM**, Volume 16 Issue 3

Full text available: 📄 pdf(1.13 MB)   Additional Information: full citation, abstract, references, citings

PL/C is a compiler for a dialect for PL/I. The design objective was to provide a maximum degree of diagnostic assistance in a batch processing environment. For the most part this assistance is implicit and is provided automatically by the compiler. The most remarkable characteristic of PL/C is its perseverance—it completes translation of every program submitted and continues execution until a user-established error limit is reached. This requires that the compiler repair errors encoun ...

Keywords: PL/I, compilers, debugging, programming languages

22 Experience Using Multiprocessor Systems—A Status Report
Anita K. Jones, Peter Schwarz
June 1980 **ACM Computing Surveys (CSUR)**, Volume 12 Issue 2

Full text available: 📄 pdf(4.48 MB)   Additional Information: full citation, references, citings, index terms

23 Prefetching using Markov predictors
Doug Joseph, Dirk Grunwald
May 1997 **ACM SIGARCH Computer Architecture News , Proceedings of the 24th annual international symposium on Computer architecture**, Volume 25 Issue 2

Full text available: 📄 pdf(1.68 MB)   Additional Information: full citation, abstract, references, citings, index terms

Prefetching is one approach to reducing the latency of memory operations in modern computer systems. In this paper, we describe the *Markov prefetcher.* This prefetcher acts as an interface between the on-chip and off-chip cache, and can be added to existing computer designs. The Markov prefetcher is distinguished by prefetching *multiple reference predictions* from the memory subsystem, and then prioritizing the delivery of those references to the processor.This design results in a pr ...

24 Migrating a CISC computer family onto RISC via object code translation

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